

101.4
(PP42)

Yale University, USA

Classical NEURON Specification Style

PreCell PostCell

PreSyn NetCon PostSyn

nc = new NetCon(PreSyn, PostSyn)

The Parallel Problem

PreCell CPU 2 CPU 4 PostCell

PreSyn NetCon PostSyn

PreCell does not exist

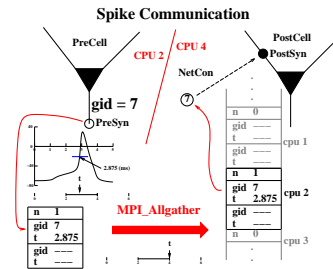


Diagram illustrating the mapping of a Halftap operation to a GPU kernel.

Top Section (Halftap Operation):

- Left side: `pc.source_var(s1, v(x1, 1))`
- Right side: `pc.target_var(g2, vpre, 1)`
- Red arrow labeled **1** connects `v(x1, 1)` to `vpre`.
- Red arrow labeled **2** connects `vpre` to `pc.target_var(g1, vpre, 2)`.
- Bottom right: `pc.source_var(s2, v(x2, 2))`

Bottom Section (GPU Kernel Code):

gap.mod

NEURON {	ASSIGNED {
POINT_PROCESS Halftap	v (millivolt)
ELECTRODE_CURRENT 1	vgap (millivolt)
RANGE x, 1, vpre	f (nanampere)
PARAMETER x = 1e9 (megohm)	BREAKPOINT { f = (vgap - v) / r }

Red arrows indicate the mapping from the Halftap operation to the kernel code:

- Red arrow labeled **1** connects `v(x1, 1)` to `v`.
- Red arrow labeled **2** connects `vpre` to `vgap`.

7 cells 3 cpus (or heterogeneous cells)

Round Robin

1 2 3

Fill cpu i ... up to (total complexity) / ncpu

1 2 3

overflow on cpu i + 1

pc = new ParallelContext()

gid = 7

gid = 9

Every spike source (cell) must have a global id number.

```
for (pc = pc.id; gid < ncells; gid += pc.nshosts) {  
    pc.set_gidnode(gid, pc.id);  
}
```

CPU 0		CPU 3		CPU 4	
gid	pcid	gid	pcid	gid	pcid
5	0	5	3	4	4
6	0	6	3	5	4
7	0	7	3	6	4
8	0	8	3	7	4
9	0	9	3	8	4

Spike exchange buffer compression (Requires fixed step method)

Reduce integration interval to < 256 dt steps, code the double spiketime as a byte.

If there are < 256 cells on each CPU code the int gid as a char local_id.

Select reasonable MPI_Alghover buffer size to send n spikes before requiring an MPI_Alghover overflow message.

Bin Queue (Requires fixed step method)

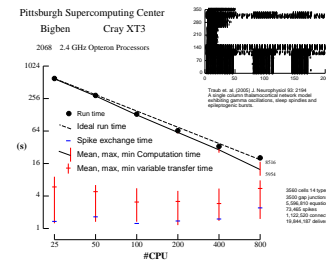
need at least maximum NetCon delay / dt bins

ARTIFICIAL_CELL SelfEvents bypass queue

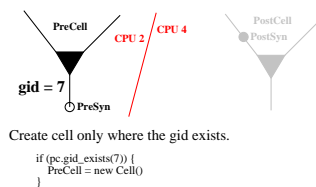
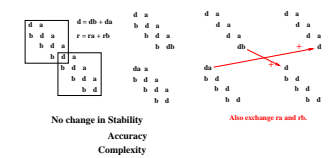
(Requires the integration interval be \leq the positive global minimum NetCon delay)

On every incoming NetCom event check to see if $\text{SelfEvent} < t$

After each integration interval iterate over outstanding SelfEvents to deliver all that are $< t$.



Any tree can be split into two subtrees with a shared root node.



Create cell only where the gid exists.

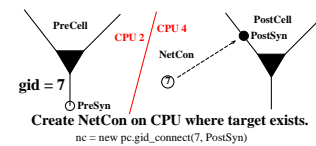
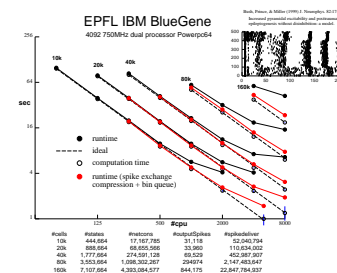
```

if (pc.gid_exists(7)) {
    PreCell = new Cell()
}

```

Associate gid with spike source.

```
nc = new NetCon(PreSyn
pc.cell(7, nc)
```



Run using the idiom

```
pc.set_maxstep(10)
stdinit()
pc.psolve(tstop)
```

any spike here must be delivered here

minimum delay

exchange exchange

pc.set_maxstep() uses
MPI_Allreduce
to determine minimum delay.

Figure 1 consists of two line graphs, (a) and (b), showing the relationship between the number of CPUs (x-axis, log scale from 1 to 1000) and the number of spikes per cell (y-axis, log scale from 10^1 to 10^4). Graph (a) shows results for 56K cells, and graph (b) shows results for 100K cells. Both graphs compare 'realtime' (solid line with circles) and 'compression' (dashed line with circles) methods. A horizontal dashed line represents the 'ideal' case. In both graphs, the 'realtime' method shows a sharp increase in spikes per cell as the number of CPUs increases, while the 'compression' method remains relatively constant. The 'ideal' line is also shown for reference.

Graph (a) 56K cells: The y-axis is labeled 'spikes/cell' and ranges from 10^1 to 10^4 . The x-axis is labeled '#CPU' and ranges from 1 to 1000. The 'realtime' method (solid line with circles) shows a sharp increase in spikes per cell as the number of CPUs increases, reaching approximately 10^4 spikes/cell at 1000 CPUs. The 'compression' method (dashed line with circles) remains relatively constant, around 10^2 spikes/cell. The 'ideal' line (dashed horizontal line) is at approximately 10^2 spikes/cell. A label '65536' is placed near the 'realtime' line at 1000 CPUs.

Graph (b) 100K cells: The y-axis is labeled 'spikes/cell' and ranges from 10^1 to 10^4 . The x-axis is labeled '#CPU' and ranges from 1 to 1000. The 'realtime' method (solid line with circles) shows a sharp increase in spikes per cell as the number of CPUs increases, reaching approximately 10^4 spikes/cell at 1000 CPUs. The 'compression' method (dashed line with circles) remains relatively constant, around 10^2 spikes/cell. The 'ideal' line (dashed horizontal line) is at approximately 10^2 spikes/cell. A label '362144' is placed near the 'realtime' line at 1000 CPUs.

Legend:
 ● realtime
 ○ compression
 --- ideal

Each cell fires randomly every 10 to 20 ms.
 56K cells, 1000 random connections per cell
 100K cells, 10,000 random connections per cell

tstep = 200ms
 delay = 10ms
 weight = 0

Supported by NINDS NS11613

Henry Markram for the use of the EPFL IBM BlueGene and development support under the auspices of the Blue Brain project.

Roger Traub for making his model available in ModelDB.

Terry Sejnowski for the use of the SDSC DataStar.

Pittsburgh Supercomputer Center for the use of the Cray XT3.

